

System Engineering Laboratory (Room No.: CE - 6)

Lab in Charge: Prof. Bhagirath P. Prajapati

Lab Assistant: Mr. Jignesh Patel Lab Area: 22' 0" X 27' 0"

Domain

- 1) Compiler Design
- 2) System Programming
- 3) OOP with C++
- 4) Information Security
- 5) OOP with Java
- 6) Database Management Systems
- 7) Computer Programming and Utilization
- 8) .NET

Computer System details

Total Number of Computer Systems: 36/

Hardware Configuration

- Acer Veriton M200 Core i7 4770 Processor
- Intel H81 chipset Motherboard
- 4GB DDR3 RAM
- 500 GB HDD SATA
- 18.5" Acer LED

Software Accessible

- Windows 8SL preloaded License
- Visual Studio .Net 2008
- Rational Rose Enterprise Studio
- Oracle Client 9i
- SQL Server 2005
- Turbo C
- Visual Studio 6.0
- Net Beans IDE 6.1
- Jcreator Pro.
- Adobe Acrobat Reader
- Microsoft Visual Studio.Net 2008